

PATENT

DOCKET NO.: Intel 2207/760602
ASSIGNEE: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT : Tom ALTUS et al.

DIVISIONAL OF
SERIAL NO. : 09/475,456

PARENT
APPLICATION
FILED : December 30, 1999

FOR : **SYSTEM**
[REDACTED] AND METHOD FOR EFFICIENT HARDWARE
IMPLEMENTATION OF A PERFECT PRECISION BLENDING
FUNCTION OR EFFICIENT HARDWARE IMPLEMENTAION OF
MULTIPLYING BY A FRACTION

GROUP ART. UNIT : 2124

EXAMINER : Tan V.
MAI

M/S: PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV351180809US in an envelope addressed to: M/S PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: June 23, 2003	Signature: 
Barbara Vance	

PRELIMINARY AMENDMENT

Dear Sir:

In response to the Office Action dated September 20, 2002, please cancel claims 1-6, and 27 in this Divisional application leaving claims 7-26, and 28-30, prior to calculating fees.

Applicant elected to prosecute claims 1-6 and 27 (Group I) in the parent application and filed this Divisional application to prosecute the remaining claims, 7-26 and 28-30 (Group II). This application claims benefit of the December 30, 1999 filing date of the parent application.

Amendments to the Specification begins on page 2 if this paper.

Amendments to the Claims are reflected in the listing of claims and begins on page 3 of this paper.